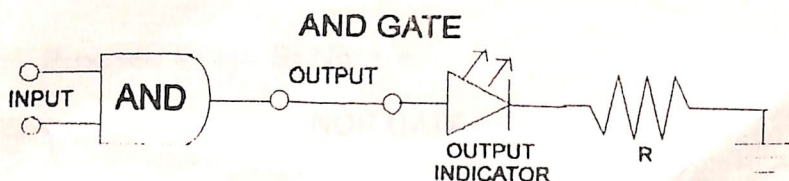




Verification of Truth Table of 'AND' GATE

1. Connect A and B inputs of AND Gate to logic inputs. '0' and '0' as shown in the truth table for AND Gate. Also connect output of AND Gate to output indicator.
2. Switch ON the instrument using OFF / ON toggle switch provided on front panel.
3. Observe the output indicator. If it glows the indication is that the output is in state '1' and if it does not glow the indication is that the output is in state '0'.
4. Similarly verify the output for other combinations of inputs 'A' and 'B' as shown in the Truth Table.

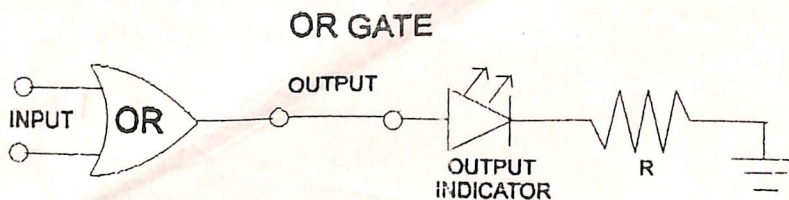
2 Input 'AND' GATE



A	B	Y
0	0	0
0	1	0
1	0	0
1	1	1

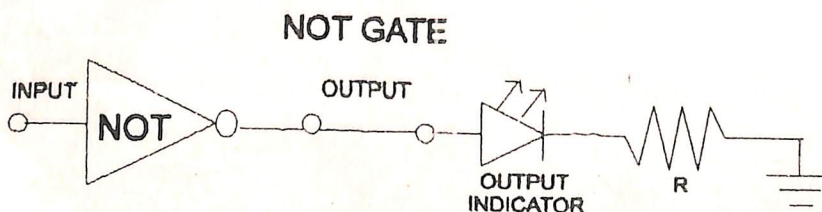
Similarly Truth Tables for 'OR' & 'NOT' Gates can be verified.

2 Input 'OR' GATE



A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1

'NOT' GATE

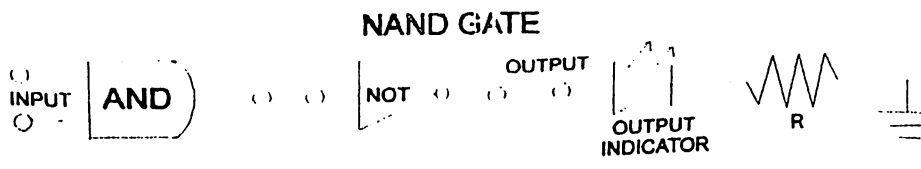


A	Y
0	1
1	0

Verification of Truth Table of 'NAND' Gate :

5. Connect output of 'AND' gate to the Input of NOT gate, Inputs of AND gate will become Inputs of NAND gate & Output of NOT gate will become Output of NAND gate.
6. Proceed as per Sr No. 1-4.

2 Input 'NAND' GATE

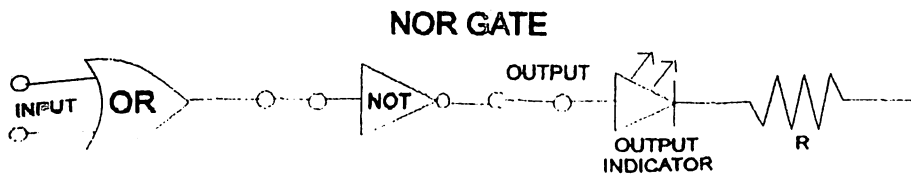


A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0

Verification of Truth Table of 'NOR' Gate :

7. Connect output of 'OR' Gate to the Input of NOT Gate, Inputs of OR Gate will become Inputs of NOR Gate & Output of NOT Gate will become Output of NOR Gate.

8. Proceed as per Sr No. 1-4.



2 Input 'NOR' GATE

A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0

Standard Accessories :

1. 4 Single point Patch Cords.
2. Instruction Manual